



COCSIT

Royal Education Society's

College of Computer Science and Information Technology (COCSIT),

COCSIT Campus, Ambajogai Road, Latur - 413512

Ph.: 02382-228646, 229191 E-mail : cocsit365@rediffmail.com



Academic Diary



Royal Education Society's

COLLEGE OF COMPUTER SCIENCE AND INFORMATION TECHNOLOGY, LATUR.

संगणकशास्त्र व माहितीतंत्रज्ञान महाविद्यालय, लातूर

COCSIT Academic Diary

Academic Year : 2017 - 2018

Semester [✓]I / [✓]III / V /

Name of Lecturer : Miss. Hawle Leela Radan

Department : Computer Science

Academic Year : 2017 - 2018

Class and Division : B.SC(CE)FY, BSC(CE)SY & Semester : I & III
M.SC(CS)FY.

Subjects : Fundamentals of Digital Logic, Data
Communication & Adv. Computer Architecture &
Microprocessors.

Semester [✓]II / IV / VI

Name of Lecturer : Hawle Leela Radan.

Department : Computer Application.

Academic Year : 2017 - 2018

Class and Division : BCAFY-A, BCAF-B Semester : II

Subjects : Fundamentals of Digital Logic.

Personal Details

1. Name of the Lecturer : Miss. Hawle Leela Rata
2. Department : Computer Science
3. Designation : Asst. Professor
4. Qualification : B.E (Electronics)
5. Date of Birth : 15/08/1984
6. Date of Appointment : 10/08/2006
7. Residential Address : Sham Nagar, Ambajogai
Near Jatal Hospital, Latur.
8. Contact No. : 9049104382
9. Blood Group : O+ve
10. Permanent Address : Sham Nagar, Ambajogai
Near Jatal Hospital, Latur.


Signature

Chap. No.	Chapter Name with Article	Total Periods	Date / Month	Remarks
1.	<u>Data Communication Concepts:</u>	(11)	3/7/2017	
	Introduction:		4/7/2017 5/7/2017	
(a)	A Communication Model	02	6/7/2017	
(b)	Data Communication Task	01	7/7/2017	
(c)	Networks: LAN, MAN, WAN	03	8/7/2017 9/7/2017	
(d)	Wireless LAN	05	10/7/2017	
	• Client server Model		11/7/2017	
	• Peer-to-Peer Network		12/7/2017	
	• Analog Signal & Digital Signal		13/7/2017 14/7/2017	
2.	<u>Protocol Architecture /</u>	(08)	15/7/2017	
	<u>Multiplexing:</u>			
(a)	The need for Protocol	04	17/7/17	
	• Architecture		18/7/17	
	• Network Architecture		19/7/17	
	• OSI Model		20/7/17	
	• TCP/IP Reference Model		21/7/17	
(b)	Multiplexing	03		
	• FDM		22/7/17	
	• TDM		23/7/17	
(c)	Connection Oriented &	01	25/7/17	
	Connectionless.			
3.	Data & computer Communication			

University Syllabus / Chapterwise Teaching Plan

Chap. No.	Chapter Name with Article	Total Periods	Date / Month	Remarks
3.	<u>Transmission Media and N/w Topology:</u>	(10)		
(a)	Transmission media -	04	26/7/17	
	• Magnetic media		27/7/17	
	• Twisted pair		28/7/17	
	• Coaxial cable		29/7/17	
	• Fiber optics.		31/7/17	
(b)	Topologies with advantages and Disadvantages:-	05		
	• Bus, Star, Ring, Tree and Mesh.		1/8/17	
			2/8/17	
		3/8/17	2/8/17	
(c)	Infrared, Microwave.	01	5/8/17	
			7/8/17	
4.	<u>Ethernet, Circuit and Packet Switching:</u>	(09)		
(a)	Switching types -	04	8/8/17	
	• Circuit switching		9/8/17	
	• Packet switching		10/8/17	
	• Message switching		11/8/17	
(b)	Ethernet	04	12/8/17	
	• Overview of Ethernet		14/8/17	
			16/8/17	
			17/8/17	
(c)	CSMA / CD	01	18/8/17	
			22/8/17	

University Syllabus / Chapterwise Teaching Plan

Chap. No.	Chapter Name with Article	Total Periods	Date / Month	Remarks
5	Network Devices & Protocols:	(10)		
(a)	Network Devices: Hub, Switch, Repeaters, Router, Gateway, Bridge.	05	23/8/17 24/8/17 25/8/17 26/8/17 28/8/17 29/8/17 30/8/17	
(b)	Protocol: FTP, HTTP, SMTP, DNS.	04	31/8/17 1/9/17 2/9/17 3/9/17	
(c)	IP address.	01	5/9/17 5/9/17	
6	Internet & other Technologies (07)			
(a)	• Internet	04	7/9/17	
	• Internet & Intranet		8/9/17 9/9/17	
	• Internet Service Provider		11/9/17 12/9/17	
	• Email		13/9/17 14/9/17 15/9/17	
(b)	• ISDN	03	16/9/17 18/9/17 19/9/17	
	• Token Ring		20/9/17 21/9/17	
	• FDDI		22/9/17	
	Revision		23/9 29/9/2017	
	<u>References:</u>			
(1)	Local Area Network By David Keiser			
(2)	Computer N/w's by Andrew S. Tanenbaum			
(3)	Data & Computer Comm ⁿ by William Stallings			

Fundamentals
 B.S (SE) FY
 University Syllabus / Chapterwise Teaching Plan

Chap. No.	Chapter Name with Article	Total Periods	Date / Month	Remarks
		(06)		
1.	<u>Fundamental Concepts:</u>			
(a)	Introduction	01	3/7/17	
(b)	Digital Signals	01	4/7/17	
(c)	Basic Gates & derived gates AND, OR, NOT, NAND, NOR, X-OR & X-NOR.	03	6/7/17 7/7/17 8/7/17 10/7/17 10/7/17 12/7/17 13/7/17 14/7/17	
(d)	De-Morgan's theorem's	01	15/7/17 17/7/17	
2.	<u>Number System and codes</u>			
(a)	Introduction to Number System	01	18/7/17	
(b)	Decimal, Binary, Octal, Hexadecimal	03	19/7/17 20/7/17 21/7/17	
(c)	Conversion from one number system to another number system	03	26/7/17	
(d)	Binary Arithmetic: Addition, Subtraction, Multiplication, Division	03	27/7/17 28/7/17 29/7/17	
(e)	Half adder & Full adder	01	31/7/17 1/8/17	
(f)	1's and 2's Complement Method	03	2/8/17 4/8/17 5/8/17	
(g)	Codes: BCD, Excess-3, Gray code		6/8/17 8/8/17	
(h)	Error detecting & correction Codes.	03	9/8/17 11/8/17	
3.	<u>Combinational Logic Design:</u>			
(a)	Standard Representation of Logic functions: SOP & POS Forms.	03	12/8/17 13/8/17	

Chap. No.	Chapter Name with Article	Total Periods	Date / Month	Remarks
(b)	K-Map Representation of Logical functions.	02	14/8/17	
			15/8/17	
(c)	Simplification of Logical functions using K-Map.	04	16/8/17	
			21/8/17	
(d)	Multiplexer & De-Multiplexer	03	22/8/17	
			23/8/17	
(e)	Encoder & De-Decoder	02	25/8/17	
			26/8/17	
4. Flip-flops				
(a)	1-bit memory cell	01	28/8/17	
(b)	Clocked SR-FF	01	29/8/17	
			30/8/17	
(c)	JK-FF	01	31/8/17	
(d)	Master-slave JK-FF	01	1/9/17	
			2/9/17	
(e)	D-type FF	01	4/9/17	
(f)	T-type FF	01	5/9/17	
5. Sequential Logic Design				
(a)	Registers	01	6/9/17	
(b)	Shift Registers	02	9/9/17	
(c)	Counters: Synchronous & Asynchronous Counter	03	11/9/17	
			15/9/17	
6. Timing Circuits, Converters & Introduction to Microprocessors				
(a)	Digital-to-Analog Converter	02	18/9/17	

University Syllabus / Chapterwise Teaching Plan

Chap. No.	Chapter Name with Article	Total Periods	Date / Month	Remarks	Chap. No.
(b)	Analog-to-Digital Converter	02	19/9/17		
(c)	I/O buses	01	20/9/17		1
(d)	Block diagram of Microprocessor	02	21/9/17		
	Revision		23 rd 9/17		
			24/9/17		
<u>References:</u>					
1)	Modern Digital Electronics				
	by R.P. Jain				
2)	Microprocessor by B-RAM				
3)	Fundamentals of Computer				
	by P.K. Singha				
					2
					3

Date	Details of topic(s) taught	No. of Present Stud.	Remarks
11/12	} Introduction to Syllabus	05	
11/12		20	
11/12	Number System —	16	
11/12	Decimal, Binary	23	
11/12	Octal & Hexadecimal	24	
11/12	Conversions — Decimal to Binary	25	
11/12	Decimal to Octal	38	
11/12	Decimal to Hexadecimal	37	
11/12	Decimal to Binary	34	
11/12	Binary to Octal & Hexadecimal	38	
11/12	Octal, Hexa to Binary	33	
11/12	Octal to Hexa and vice-versa	27	
11/12	Octal, Binary & Hexa to	40	
11/12	Decimal — Conversion	45	
11/12	Half adder and full adder	42	
11/12	Binary Arithmetic: Addition & Subtraction	45	
11/12	} Binary Multiplication and	38	
11/12		division	31
11/12	Codes — BCD code	40	
11/12	Gray code: Gray to Binary & vice-versa	39	
11/12	Excess-3 code	40	
11/12	} Parity code	41	
11/12		29	
11/12	} Hamming code	29	
11/12		48	

Daily Report

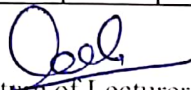
Class: August Subject: DE Time: 3:30 PM Lect. Sign.: Joh
4:15 PM

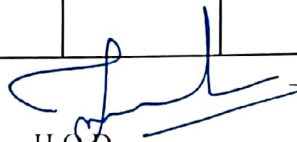
Date	Details of topic(s) taught	No. of Present Stud.	Remarks
11/8/17	Logic Gates Basic gates - AND, OR	45	
21/8/17	NOT (2) Universal - NAND & NOR gates	46	
31/8/17	special purpose - Ex-OR & Ex-NOR	48	
7/8/17	Revision of all gates.	51	
8/8/17	What is signal? Types of signals.	55	
9/8/17	? De-Morgan's I and II	54	
10/8/17	} theorems.	56	
11/8/17	? Revision of chapter.	58	
12/8/17	}	16	
14/8/17	Introduction to K-Map.	49	
16/8/17	• SOP Method. Formats.	43	
18/8/17	? Problems on SOP Method.	48	
19/8/17	} K-Map.	39	
22/8/17	? Use of Don't care condition	52	
23/8/17	} in SOP Method.	50	
24/8/17	• POS method Format.	55	
26/8/17	? Problems on POS Method of	54	
28/8/17	} K-Map.	50	
29/8/17	? Multiplexer & its types -	45	
31/8/17	} 4:1, 8:1 & 16:1 MUX.	25	
1/9/17	Encoder and its type	35	
4/9/17	Decoder and its type	51	
5/9/17	1-bit Memory cell	37	
6/9/17	SR-FF & JK-FF	60	
7/9/17	T-type FF and D-type FF	59	


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Syllabus Progress/Completion Report

Month September

Sr. No.	Class & Division	Subjects	Practical			Theory			Date of Syllabus Completion
			Batches	Total No. of Practical's	No. of Practical's Completed in	Total No. of Chapters in Syllabus	No. of Chapters completed	Last Date of Syllabus Comp. Report Submitted	
1.	BSC(CE) F4	DE	-	-	-	06	06	28/9/17	29/9/17
2.	BSC(CE) F4	DC	-	-	-	06	06	28/9/17	29/9/17
3.	M.Sc(CS) F4	CAM	A1 A2	24	24	06	05	29/9/17	4/10/17


Signature of Lecturer


H.O.D.
Signature with date

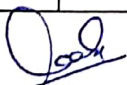

Principal
V.C. PRINCIPAL
College of Computer Science
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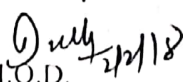
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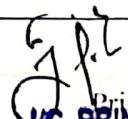
Syllabus Progress/Completion Report

Month Jan: January 2018

Sr. No.	Class & Division	Subjects	Practical			Theory			Date of Syllabus Completion
			Batches	Total No. of Practical's	No. of Practical's Completed in	Total No. of Chapters in Syllabus	No. of Chapters completed	Last Date of Syllabus Comp. Report Submitted	
1.	BAMA	DE	-	-	-	06	12/3	31/1/2018	25/3/2018
2.	BAFIS	DE	-	-	-	06	12/3	31/1/2018	25/3/2018


Signature of Lecturer


H.O.D.
Signature with date

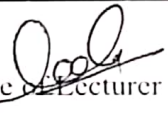

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Syllabus Progress/Completion Report

Month February

Sr. No.	Class & Division	Subjects	Practical			Theory			Date of Syllabus Completion
			Batches	Total No. of Practical's	No. of Practical's Completed in	Total No. of Chapters in Syllabus	No. of Chapters completed	Last Date of Syllabus Comp. Report Submitted	
1.	BCA FY A	DE	-	-	-	06	3½	28/2/18	20/3/18
2.	BCA FY B	DE	-	-	-	06	3½	28/2/18	20/3/18
3.	B-SCNT FY	DE	-	-	-	06	3½	28/2/18	20/3/18

Signature  Lecturer

Duty  12/3/18
H.O.D.
Signature with date

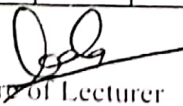

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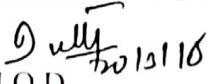
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Syllabus Progress/Completion Report

Month March 2018

Sr. No.	Class & Division	Subjects	Practical			Theory			Date of Syllabus Completion
			Batches	Total No. of Practical's	No. of Practical's Completed in	Total No. of Chapters in Syllabus	No. of Chapters completed	Last Date of Syllabus Comp. Report Submitted	
1.	BCA FY A	DE	-	-	-	06	06	17/3/2018	
2.	BCA FY B	DE	-	-	-	06	06	17/3/2018	
3.	B-SCNT FY	DE	-	-	-	06	06	17/3/2018	

Signature  of Lecturer


Duty  20/3/18
H.O.D.
Signature with date


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Instructions

- 1) The teacher will be responsible for the sole custody of this diary for the academic year. He/She shall submit it, when ever required, to the principal or the concerned authority. The diary shall remain in the custody of college after the completion of the academic year.
- 2) The Topics covered in each class should be recorded regularly in the 'Daily Record'. The principal/HOD shall attest the entries in this Dairy at the end of every month.
- 3) The Staff should submit the progress report of the syllabus at the end of every month.
- 4) The designated authority shall prepare the consolidated report at the end of the each academic term for each class.
- 5) After checking and endorsing the entries the designated member of the staff shall forward the syllabus completion repot to the Principal for further action.
- 6) This Diary should be returned to the Principal whenever the staff member leaves the college at the end of the academic semester / year.


Principal
D.C. PRINCIPAL
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